SMPTE ROADMAP

SMPTE Bit-Serial Interfaces at 3 Gb/s – Roadmap for the 425 Document Suite

Document Roadmap

The SMPTE 425 suite of documents defines the mapping of various source image formats onto a single link, dual link and quad link serial digital interfaces operating at a nominal rate of 3 Gb/s. This informative "roadmap" describes the documents in the SMPTE 425 suite. The SMPTE 425 series documents specify a virtual interface that is carried on a physical link specified by SMPTE ST 424.

1 SMPTE ST 425-1

SMPTE ST 425-1 defines the mapping of various source image formats onto a single link serial digital interface operating at a nominal rate of 3 Gb/s. This standard defines three mapping formats: Level A, Level B Dual-Link mapping and Level B Dual-Stream mapping as described below.

Level A specifies the direct mapping of various uncompressed video image formats and the direct mapping of packetized data into a serial digital interface operating at a nominal rate of 3 Gb/s. It also defines the carriage of ancillary data such as the audio data, the audio control packets, the payload ID, the time code, etc., for these direct mappings.

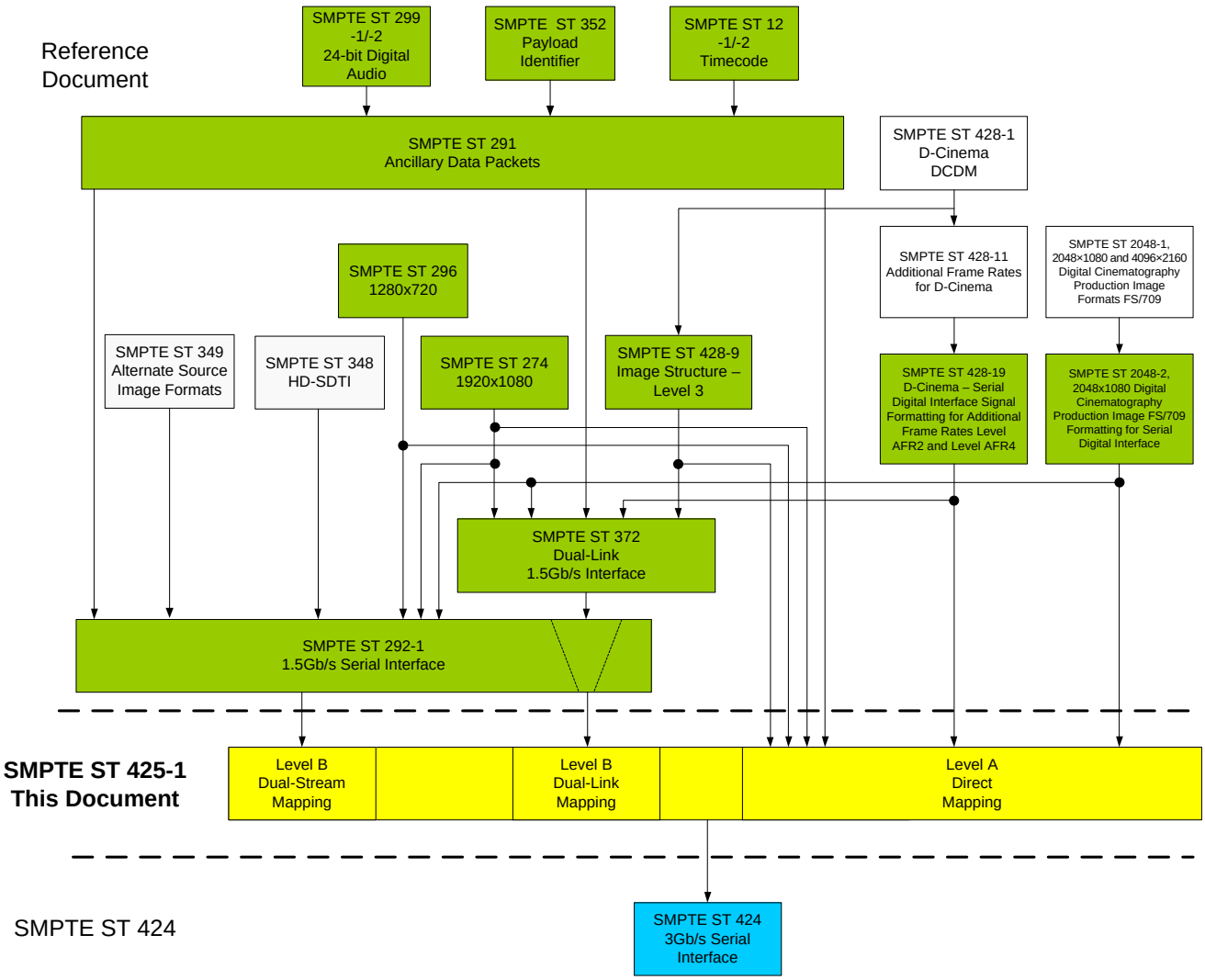
Level B Dual-Link mapping specifies the mapping of the SMPTE ST 372 Dual Link 1.5 Gb/s interface into a serial digital interface operating at a nominal rate of 3 Gb/s.

Level B Dual-Stream mapping specifies the mapping of two of the SMPTE ST 292-1 1.5 Gb/s HD-SDI interfaces into a serial digital interface operating at a nominal rate of 3 Gb/s.

Uncompressed video image formats or packetized data, and all applicable ancillary data such as the audio data, the audio control packets, the payload ID, the time code, etc., shall be mapped into SMPTE ST 372 Dual Link and 2 x SMPTE ST 292-1 interfaces prior to mapping into the virtual interfaces.

Approved
March 30, 2011

1.1 SMPTE ST 425-1 Document Road Map illustrating “Data Flow Hierarchy”



Document Road Map Color Legend

- Normative reference document
- Informative document
- This document
- Referencing document