

SMPTE STANDARD

12 Gb/s Signal/Data Serial Interface — Electrical — Amendment 1



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Foreword

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Amendment 1 to SMPTE ST 2082-1:2015 was prepared by Technology Committee 32NF.

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1 Scope

The purpose of this amendment is to change the Timing Jitter specification for the interface to correctly reflect the requirements and capabilities of 12G-SDI equipment. It also corrects arithmetic errors in the alignment jitter specification where the time representation of 0.3UI is inaccurately calculated in the standard.

Change instructions are shown in *italics*. Inserted text is shown thus. Deleted text is shown ~~thus~~.

2 Amendment of Section 5 Coaxial Cable Interface

In Table 1 – Jitter Specifications change the table as follows:

Table 1 – Jitter Specifications

f3	100 kHz	Alignment Jitter lower band edge
f4	> 1/10 th the clock rate (>1188 MHz)	Upper band edge
A1	2UI (168psec) <u>8 UI (674ps)</u>	Timing jitter expressed
A2	0.3 UI (28psec) <u>(25.3ps)</u>	Alignment jitter (UI = unit interval. 1 UI = 84 psec). Note 1
Test Signal	Color bar test signal	Note 2